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Docket No.: 1190-0756PUS1
(Patent)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:
Satoshi YAMANAKA et al.

Application No.: 10/541,611

Confirmation No.: 8340

Filed: July 07, 2005

Art Unit: 2625

For: PIXEL INTERPOLATION CIRCUIT, PIXEL
INTERPOLATION METHOD AND IMAGE
READER

Examiner: R. Z. Zhu

REPLY UNDER § 1.116

MS AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INTRODUCTORY COMMENTS

In reply to the Office Action dated September 17, 2010, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Claims.

Remarks.